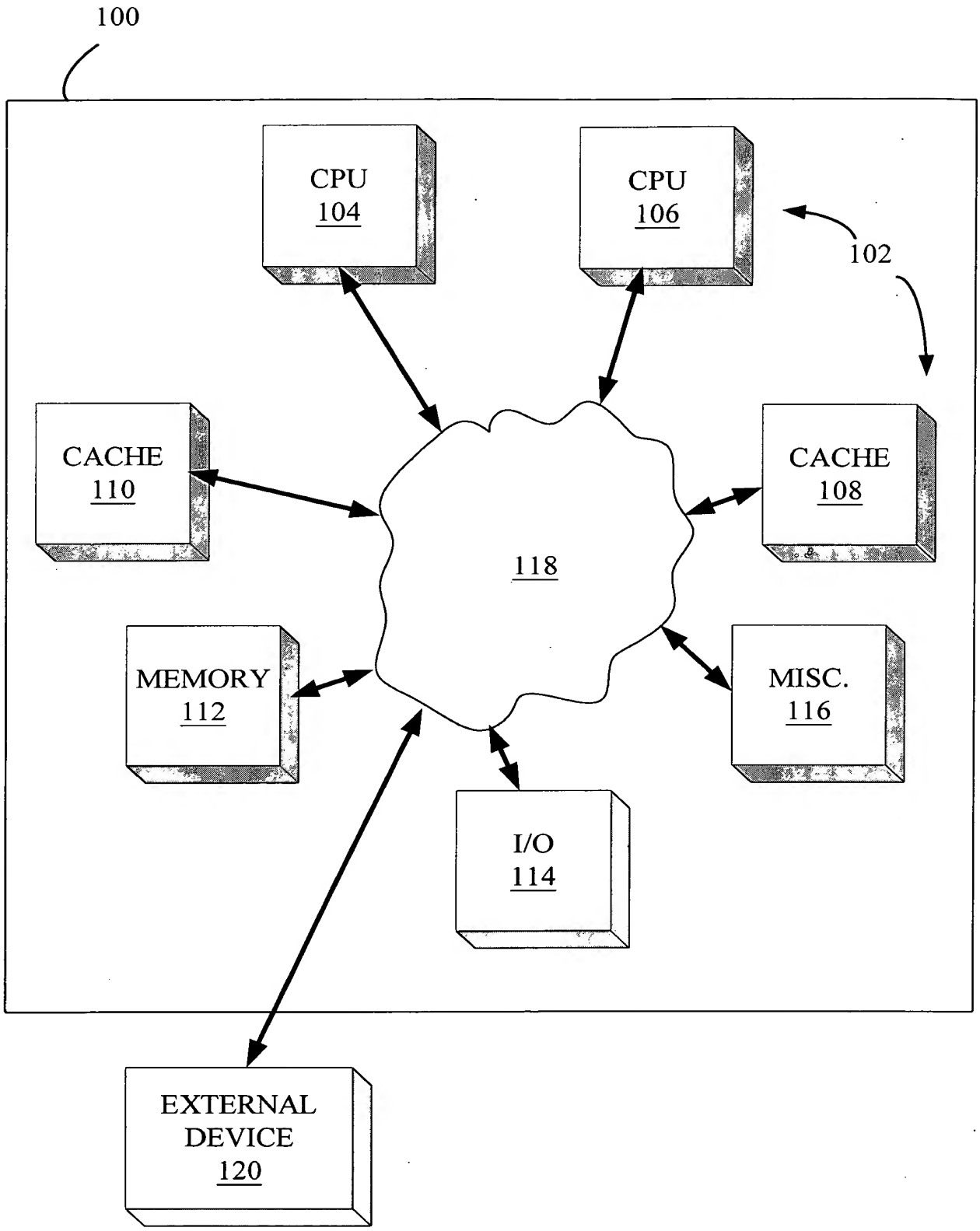

100
CPU
104
CPU
106
102
CACHE
110
CACHE
108
118
MEMORY
112
MISC.
116
I/O
114
EXTERNAL
DEVICE
120

Figure 1

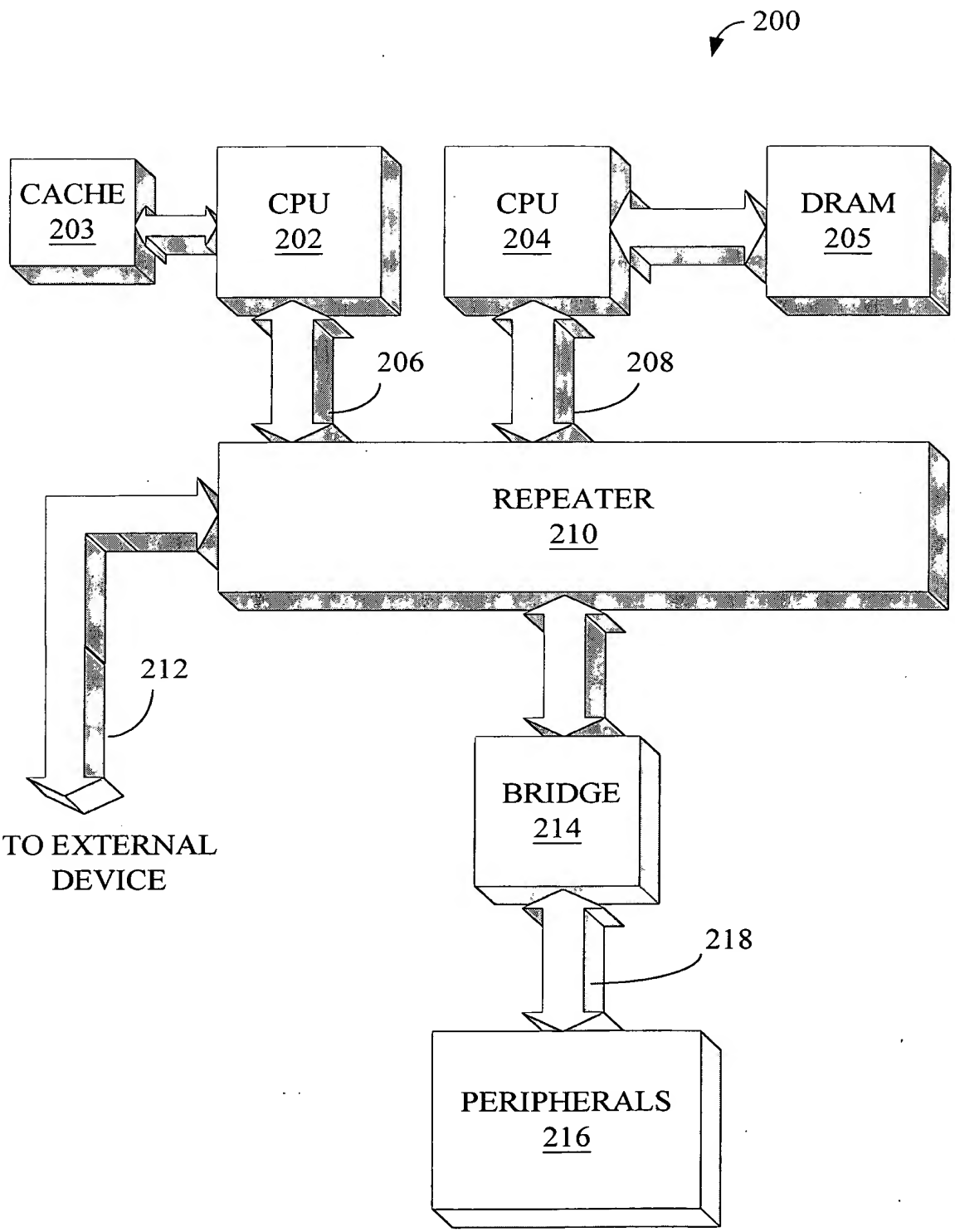

200
CACHE
203
CPU
202
CPU
204
DRAM
205
206
208
REPEATER
210
212
TO EXTERNAL
DEVICE
BRIDGE
214
218
PERIPHERALS
216

Figure 2

Figure 3

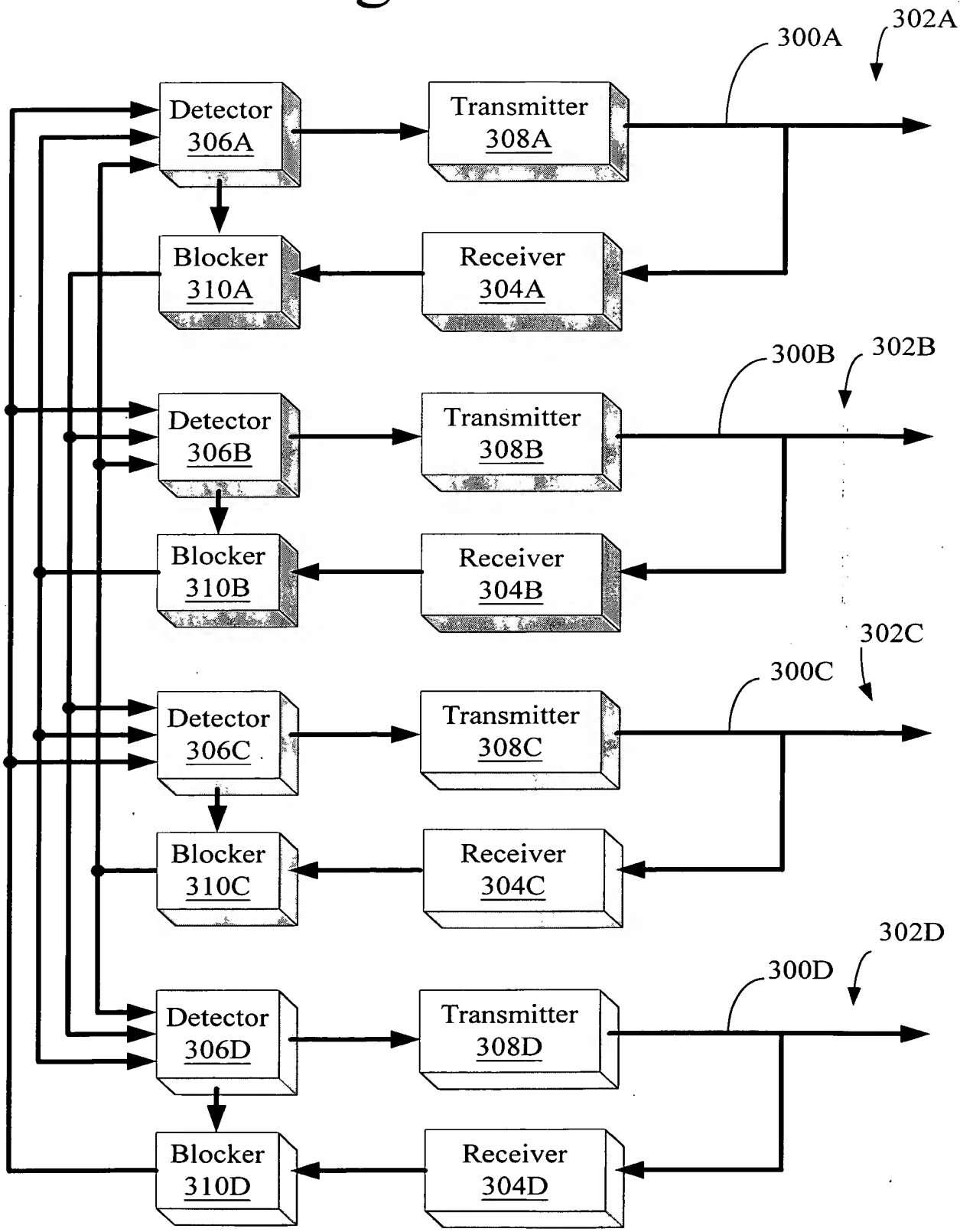
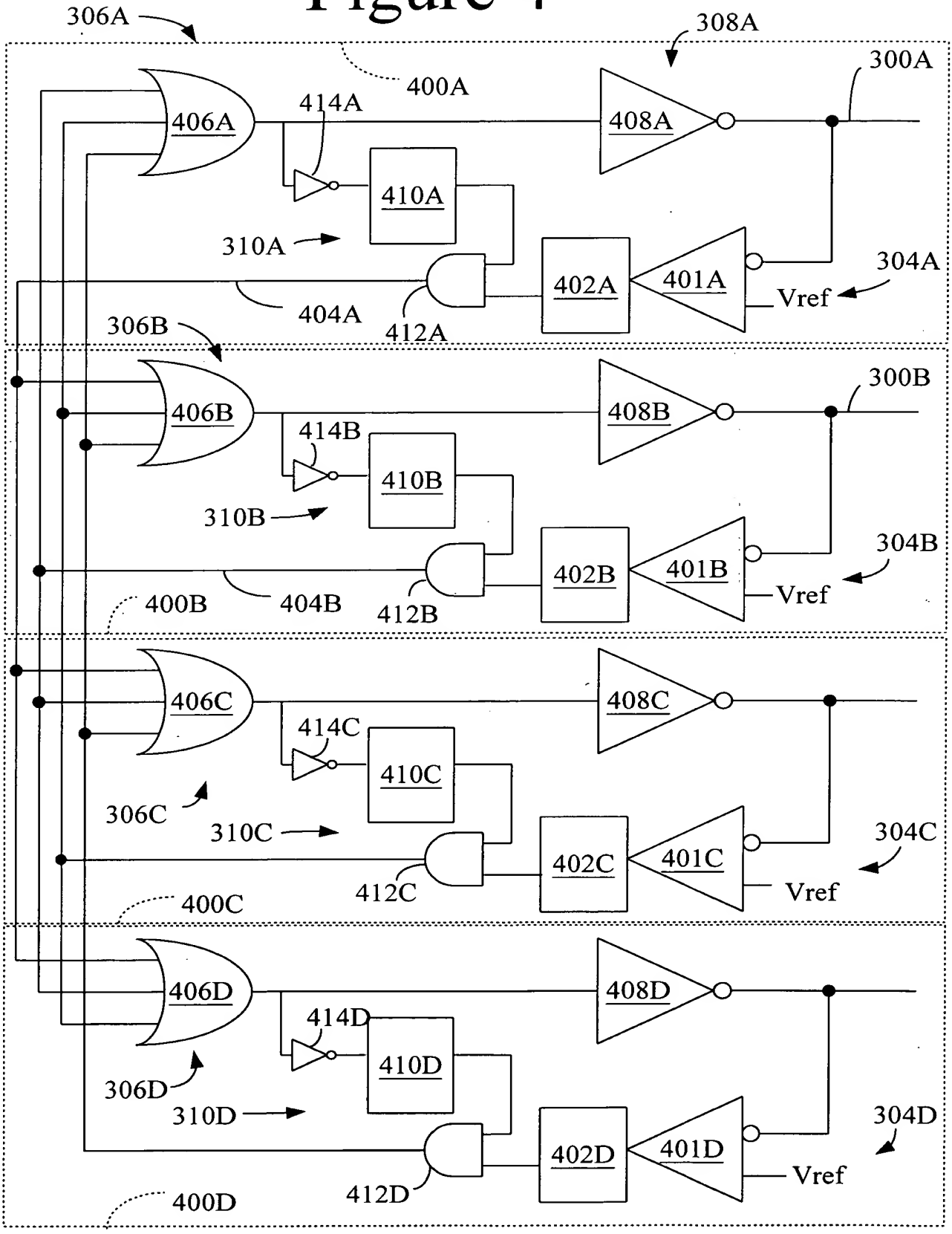

300A
302A
Detector 306A
Transmitter 308A
Blocker 310A
Receiver 304A
300B
302B
Detector 306B
Transmitter 308B
Blocker 310B
Receiver 304B
302C
300C
Detector 306C
Transmitter 308C
Blocker 310C
Receiver 304C
302D
300D
Detector 306D
Transmitter 308D
Blocker 310D
Receiver 304D

Figure 4


306A
308A
300A
400A
406A
414A
408A
410A
310A
402A
401A
304A
Vref
404A
412A
306B
406B
300B
414B
408B
410B
310B
304B
402B
401B
Vref
400B
404B
412B
406C
408C
414C
410C
306C
310C
304C
402C
401C
Vref
412C
400C
406D
408D
414D
410D
306D
310D
304D
402D
401D
Vref
412D
400D